

Amendment and Response

Applicant: David C. Chu et al.

Serial No.: 10/668,851

Filed: September 23, 2003

Docket No.: 10030919-1 (A310.167.101)

Title: PHASE DIGITIZER FOR SIGNALS IN IMPERFECT QUADRATURE

IN THE SPECIFICATION

Please replace the paragraph starting at page 12, line 19 with the following rewritten paragraph:

Figure 3 is a block diagram illustrating an exemplary embodiment of phase digitizer 200. Phase digitizer 200 digitally processes incoming inphase signal V 148 and quadrature signal U 142. Phase digitizer 200 includes analog-to-digital converters (ADCs) 212 and 214, digital signal processor 202, and phase accumulator 208. Phase digitizer 200 uses a block regression technique for phase digitizing in the steady state, wherein linear regression processing is applied to selected sums of the digital signal waveform samples.